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**PATENT****IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application No.: 10/761,239

Filing Date: January 22, 2004

Applicant: Jong-Hyun CHOI

Title: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE WITH  
DUAL INSULATION SYSTEM (AS AMENDED)

Attorney Docket: 8947-000068/US

Group Art Unit: 2816

Examiner: Terry Lee Englund

Customer Service Window  
Randolph Building  
401 Dulany Street  
Alexandria, VA 22314  
**Mail Stop AF**

January 6, 2009

**AMENDMENT**

Sir:

In response to the Office Action mailed October 6, 2008, the following amendments and remarks, are respectfully submitted in connection with the above-identified application.

**Amendments to the Claims** begin on page 2 of this Amendment.**Remarks** begin on page 9 of this Amendment.

	<b>Claims remaining after</b>		<b>Highest number previously paid</b>		<b>Present extra</b>
<b>Total</b>	10	-	37	=	0
<b>Independent</b>	1	-	4	=	0